

REMARKS

Claims 1-7 and 15-19 are pending in this application. By this Amendment, claim 1 is amended, claims 8-14 are canceled without prejudice or disclaimer, and claim 19 is added. Support for the amendment to claim 1 and new claim 19 can be found in the specification, for example, at page 22, line 3 - page 27, line 18. No new matter is added.

Claims 1, 3-9, 13 and 14 are rejected under 35 U.S.C. §102(b) over Murata (U.S. Patent No. 6,111,659); and claims 2, 10, 12 and 15-18 are rejected under 35 U.S.C. §103(a) over Murata in view of Yoneta et al. (U.S. Patent No. 6,359,699). The rejections of canceled claims 8-14 are moot and the rejections of the remaining claims are respectfully traversed.

As an initial matter, Applicant notes that the Office Action and Advisory Action are unclear regarding which feature allegedly corresponds to the claimed terminal device and data processing device of previously presented independent claim 1. For example, the Advisory Action states "rather, as explained in the corresponding parenthetical, the PC card controller [88] is connected to and communicates with the CPU [85] of the data processing device via an interface [83], as shown in figure 2 of Murata" (see Advisory Action, page 2, emphasis added). This statement would suggest that the Office Action is interpreting the CPU 85 and bus 83 correspond to the claimed data processing device. However, the Advisory Action further states "[t]hus, it is the data processing device connected via the interface that the Examiner was stating to be the terminal device, not the PC Card Controller" (see Advisory Action, page 2). This statement would suggest that the Office Action is interpreting the CPU 85 and CPU bus 83 correspond to the claimed terminal device.

Further, the Advisory Action asserts that the PC Card Controller 88 and the CPU bus 83, in combination, correspond to the claimed interface of independent claim 1 (see Advisory Action, page 3). Under this interpretation, the Examiner would thus be asserting that the CPU bus 83 of Murata corresponds to, at least in part, both the terminal unit and the interface

of independent claim 1. However, previously presented independent claim 1 was directed to a data processing device with a terminal device (now recited as an external personal computer) connected to the data processing device via an interface. It is unclear how the Examiner's interpretation of the features in Murata achieve this claimed configuration.

Further, Murata does not teach every claimed feature of independent claim 1. Murata does not teach "a function implementing unit; and an interface that is structured to be connected to both an external personal computer and the function implementing unit, the function implementing unit accessing to the storing unit to read and write various types of data via the interface," as recited in independent claim 1 (emphasis added).

The Office Action asserts that the memory card of Murata corresponds to the claimed storage unit of independent claim 1 (see Office Action, pages 2 and 3). Applicant respectfully disagrees.

For example, Murata does not teach the claimed interface. As explicitly stated by the Advisory Action, the PC Card Controller 88 and the CPU bus 83, in combination, correspond to the claimed interface of independent claim 1 (see Advisory Action, page 3). However, while the print command file of Murata (i.e. the alleged instruction data) is allegedly written into the memory card by the user's personal computer, the print command file is not so written via the PC Card Controller 88 and CPU bus 83 (see col. 6, line 22 - col. 8, line 51 of Murata). Therefore, Murata does not teach "the function implementing unit accessing to the storing unit to read and write various types of data via the interface," as recited in independent claim 1 (emphasis added).

Further, none of the applied references teach "wherein the function implementing unit that, when instruction data is stored in the storing unit via the interface by the external personal computer, reads the instruction data," as recited in independent claim 1 (emphasis added). Rather, the digital copying machine of Murata reads the print command file from the

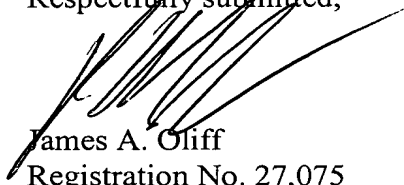
memory card inserted into the PC card slot 89, but that command file is not stored in the memory card via the PC Card Controller 88/CPU bus 83 (i.e., the alleged interface).

Therefore, for at least these reasons, independent claim 1 is patentable over the applied references. Claims 2-7 and 9-12, which depend on independent claim 1, are also patentable for at least their dependency on independent claim 1, as well as for the additional features they recite. Applicant thus respectfully requests withdrawal of the rejections.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

Patrick T. Muffo
Registration No. 60,342

JAO:PTM/hs

Date: March 4, 2009

Attached: Petition for Extension of Time
Request for Continued Examination

OLIFF & BERRIDGE, PLC
P.O. Box 320850
Alexandria, Virginia 22320-4850
Telephone: (703) 836-6400

<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
--